

Application Number 10/644,494

Amendment responsive to Office Action mailed July 25, 2005

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application.

Listing of Claims:

Claim 1 (Currently amended) A memory card comprising:

a memory;

a first connector electrically coupled to the memory and conforming to a first connector standard; and

a second connector electrically coupled to the memory and conforming to a second connector standard, wherein the first connector standard comprises a host computer connector (HCC) standard and the second connector standard comprises a device communication connector (DCC) standard; and

a controller that controls the memory and controls output via the first connector and the second connector, wherein the first and second connectors are electrically coupled to the memory through the controller and wherein the controller comprises a memory controller integrated with a first connector controller conforming to the first connector standard and integrated with a second connector controller conforming to the second connector standard.

Claims 2-6 (Canceled).

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Claim 7 (Currently amended) The memory card of claim 1-6, wherein:

the HCC comprises a standard selected from a group consisting of: a personal computer memory card international association (PCMCIA) standard, a PC Card standard, a CardBus standard, a Universal Serial Bus (USB) standard, a Universal Serial Bus 2 (USB2) standard, an IEEE 1394 FireWire standard, a Small Computer System Interface (SCSI) standard, an Advance Technology Attachment (ATA) standard, a serial ATA standard, a Peripheral Component Interconnect (PCI) standard, and a conventional serial or parallel standard; and

the DCC comprises a standard selected from a group consisting of: a Compact Flash standard, a Smart Media standard, a MultiMedia Card standard, a Secure Digital standard, a Memory Stick standard, and an xD standard.

Claim 8 (Original) The memory card of claim 1, wherein the first connector is disposed on a different side of the memory card than the second connector.

Claim 9 (Original) The memory card of claim 8, wherein the first connector is disposed on an opposite side of the memory card relative to the second connector.

Claim 10 (Original) The memory card of claim 1, wherein at least one of the first connector and the second connector comprises a retractable connector.

Claim 11 (Original) The memory card of claim 10, further comprising:

a housing defining a slot for the retractable connector; and

a first electrical contact on the retractable connector and a second electrical contact within the slot, wherein the first electrical contact couples to the second electrical contact when the retractable connector is extended from the slot.

Claim 12 (Original) The memory card of claim 10, wherein the first connector is disposed on the same side of the memory card as the second connector.

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Claim 13 (Original) The memory card of claim 12, wherein a set of electrical contact elements of the first connector comprise a subset of a set of electrical contact elements of the second connector.

Claim 14 (Canceled).

Claim 15 (Currently amended) A memory card comprising:

a memory;

a first connector electrically coupled to the memory and conforming to a first connector standard;

a second connector electrically coupled to the memory and conforming to a second connector standard, wherein the first connector standard comprises a host computer connector (HCC) standard and the second connector standard comprises a device communication connector (DCC) standard;

The memory card of claim 1, further comprising:

a first controller electrically coupled to the memory and the first connector, the first controller controlling the memory and output via the first connector, wherein the first controller comprises a memory controller integrated with a first connector controller conforming to the first connector standard; and

a second ~~connector~~-controller electrically coupled to the second connector and the first controller, the second ~~connector~~-controller controlling output via the second connector and conforming to the second connector standard, wherein the first connector is electrically coupled to the memory through the first controller, and the second connector is electrically coupled to the memory through the second ~~connector~~-controller and the first controller.

Claims 16-18 (Canceled).

Claim 19 (Original) The memory card of claim 1, further comprising a third connector electrically coupled to the memory and conforming to a third connector standard.

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Claim 20 (Original) The memory card of claim 19, further comprising a fourth connector electrically coupled to the memory and conforming to a fourth connector standard.

Claims 21-22 (Canceled).

Claim 23 (Currently amended)

A system comprising:

a first device including a first electrical contact for receiving a connector that conforms to a first connector standard;

a second device including a second electrical contact for receiving a connector that conforms to a second connector standard; and

a memory card including:

a memory,

a first connector conforming to the first connector standard such that the first connector can be received by the first electrical contact of the first device, and

a second connector conforming to the second connector standard such that the second connector can be received by the second electrical contact of the second device, wherein the first connector standard comprises a host computer connector (HCC) standard and the second connector standard comprises a device communication connector (DCC) standard, and

a controller that controls the memory and controls output via the first connector and the second connector, wherein the first and second connectors are electrically coupled to the memory through the controller and wherein the controller comprises a memory controller integrated with a first connector controller conforming to the first connector standard and integrated with a second connector controller conforming to the second connector standard.

Claim 24 (Canceled).

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Claim 25 (Currently amended) The system of claim 23, wherein the first connector standard comprises a ~~device communication connector (DCC)~~ standard selected from a group consisting of: a Compact Flash standard, a Smart Media standard, a MultiMedia Card standard, a Secure Digital standard, a Memory Stick standard, and an xD standard; and

the second connector standard comprises an ~~host computer communication connector (HCC)~~ standard selected from a group consisting of: a personal computer memory card international association (PCMCIA) standard, a PC Card standard, a CardBus standard, a Universal Serial Bus (USB) standard, a Universal Serial Bus 2 (USB2) standard, an IEEE 1394 FireWire standard, a Small Computer System Interface (SCSI) standard, an Advance Technology Attachment (ATA) standard, a serial ATA standard, a Peripheral Component Interconnect (PCI) standard, and a conventional serial or parallel standard.

Claim 26 (Canceled).

Claim 27 (New) A system comprising:

a first device including a first electrical contact for receiving a connector that conforms to a first connector standard;

a second device including a second electrical contact for receiving a connector that conforms to a second connector standard; and

a memory card including:

a memory,

a first connector conforming to the first connector standard such that the first connector can be received by the first electrical contact of the first device,

a second connector conforming to the second connector standard such that the second connector can be received by the second electrical contact of the second device, wherein the first connector standard comprises a host computer connector (HCC) standard and the second connector standard comprises a device communication connector (DCC) standard,

a first controller electrically coupled to the memory and the first connector, the first controller controlling the memory and output via the first connector, wherein the first

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controller comprises a memory controller integrated with a first connector controller conforming to the first connector standard, and

a second controller electrically coupled to the second connector and the first controller, the second controller controlling output via the second connector and conforming to the second connector standard, wherein the first connector is electrically coupled to the memory through the first controller, and the second connector is electrically coupled to the memory through the second controller and the first controller.